

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of :

Theodore Vaida et. al.

Serial No. : 09/851,181Filed : May 08, 2001For : Application Specific Integrated
Circuit Having A Programmable
Logic Core And A Method Of
Operation ThereofRECEIVED
CENTRAL FAX CENTER

APR 26 2005

Group Art Unit : 2661Examiner : Phan, Tri H.Atty Docket : LSIL-01-035 / 01-
035

I hereby certify that this correspondence is being deposited
with the U.S. Postal Service as First Class Mail in an envelope
addressed to: Commissioner for Patents, P. O. Box 1450,
Alexandria, VA 22313-1450, on the date below:
Connie Del Casillo

4/26/05 *Connie Del Casillo*
Date Signature

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

The references listed in the attached form, copies of which are attached, may be material to examination of above-identified application. Applicants submit these references in compliance with their duty of disclosure pursuant to 37 CFR 1.56 and 1.97.

It is requested that the information disclosed herein be made of record in the application.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

If it is determined that any additional fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 12-2252.

LSI Logic Corporation
1621 Barber Lane, MS D-106
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408-433-7475

Date: 4/25/2005

Respectfully submitted,

Henry Groth

Henry Groth

Reg. No. 39,696

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